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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of

Andra, et al.

Serial No.: 10/709,793

Group Art Unit: 2171

Filing Date: May 28, 2004

Examiner: Unknown

For: A SYSTEM AND METHOD FOR SPEEDING XML CONSTRUCTION FOR A  
BUSINESS TRANSACTION USING PREBUILT XML WITH STATIC AND  
DYNAMIC SECTIONS

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicants' duty of disclosure under 37 CFR §1.56, applicants respectfully bring the following documents, listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicants are aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 09-0456.

Respectfully submitted,

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**CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)**

Applicant(s): Andra, et al.

Docket No.

BUR920040087US1

Application No.

10/709,793

Filing Date

May 28, 2004

Examiner

Unknown

Customer No.

29154

Group Art Unit

2171

Invention: A SYSTEM AND METHOD FOR SPEEDING XML CONSTRUCTION FOR A BUSINESS TRANSACTION  
USING PREBUILT XML WITH STATIC AND DYNAMIC SECTIONS

I hereby certify that this **Information Disclosure Statement**

(Identify type of correspondence)

is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope

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# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

BUR920040087US1

Application Number

10/709,793

Applicant(s)

Andra, et al.

Filing Date

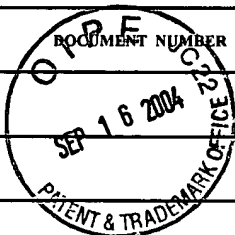
May 28, 2004

Group Art Unit

2171

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE



## U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

## FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

## OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

		Meyuhas, et al., "THE OPERATIONAL LOGIC BEHIND MANAGING A FULLY AUTOMATED 300 mm FEB", Semiconductor Manufacturing, October 2003, pages 152-159
		Girin, et al., "PROCESS CONTROL THROUGH LITHOGRAPHY AUTOMATION", Semiconductor Manufacturing, July 2003, pages 85-90

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.